



PATENT  
8031-1028

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yoshihiro NONAKA Conf. 5218

Application No. 10/648,256 Group 2811

Filed August 27, 2003 Examiner Ori Nadav

SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD OF  
MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT,  
CHARGE PUMP CIRCUIT, LAYOUT DESIGNING APPARATUS,  
AND LAYOUT DESIGNING PROGRAM

AMENDMENT

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 30, 2005

Sir:

In response to the Office Action mailed May 3, 2005,  
for which the period for response has been extended two months,  
please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this  
paper.

**Amendments to the Claims** are reflected in the listing  
of claims which begins on page 6 of this paper.

**Amendments to the Drawings** begin on page 13 of this  
paper.

**Remarks** begin on page 14 of this paper.

An **Appendix** is attached which follows the signature  
page of this paper.